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01/15/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10050347	01/15/2002	438	28.7	2813	SCHILLINGER

**APPLICANTS: Sandhu Gurtej; Moore John; Ruager Neal;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/633,556 08/07/2000

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☐ yes ☒ no

35 USC 119 conditions met

☐ yes ☒ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

MI22-1897

TITLE : Method of forming transistors

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
7/2/02		Assistant Examiner OLIV CHAUDHURI SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800 Primary Examiner PREPARED FOR ISSUE	
ISSUE FEE		DRAWING	
Amount Due 1580	Date Paid	Total Claims 12	Print Claim for O.G.
<input type="checkbox"/> TERMINAL DISCLAIMER		Sheets Drawn 3	Figs. Drawn 16
		Print Fig. 2	Application Examiner Hod 7/9/02
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